



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/604,301	96/26/2000	Yusuke Tsutsui	81784.0210 3587	
26021 7.			EXAMINER	
HOGAN & HARTSON L.L.P.			NGUYEN, HAU H	
500 S. GRAND AVENUE SUITE 1900			ART UNIT	PAPER NUMBER
LOS ANGELES, CA 90071-2611			2676	14
			DATE MAILED: 07/08/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Advisory Action	09/604,301	TSUTSUI ET AL.				
	Examiner	Art Unit				
	Hau H Nguyen	2676				
The MAILING DATE of this communication appe	ears on the cover sheet with the c	correspondence addi	ess			
THE REPLY FILED 18 June 2004 FAILS TO PLACE TH Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this application abandonment of this application abandonent which amendment which	ation. A proper reply n places the applicat	to a tion in			
PERIOD FOR RE	PLY [check either a) or b)]		•			
a) The period for reply expires <u>03</u> months from the mailing da		6				
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	ater than SIX MONTHS from the mailing S FILED WITHIN TWO MONTHS OF TH	g date of the final rejection HE FINAL REJECTION.	on. See MPEP			
Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offictimely filed, may reduce any earned patent term adjustment. See 37 C	of extension and the corresponding amo the shortened statutory period for reply ce later than three months after the mail	unt of the fee. The appropriate of the final (opriate extension Office action; or			
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFR						
$2. \boxtimes$ The proposed amendment(s) will not be entered be	ecause:					
(a) X they raise new issues that would require further consideration and/or search (see NOTE below);						
(b) they raise the issue of new matter (see Note below);						
 (c) they are not deemed to place the application in issues for appeal; and/or 	n better form for appeal by mate	rially reducing or sin	nplifying the			
(d) they present additional claims without canceling a corresponding number of finally rejected claims.						
NOTE: See Continuation Sheet.						
3. Applicant's reply has overcome the following reject	tion(s):					
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed	amendment			
5. ☑ The a) ☐ affidavit, b) ☐ exhibit, or c) ☑ request for application in condition for allowance because: See		idered but does NO	F place the			
6. The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection.	ause it is not directed SOLELY t	o issues which were	enewly			
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we			nd an			
The status of the claim(s) is (or will be) as follows:						
Claim(s) allowed:						
Claim(s) objected to:						
Claim(s) rejected: <u>1-19</u> .						
Claim(s) withdrawn from consideration:						
8. The drawing correction filed on is a) app	roved or b) disapproved by t	he Examiner.				
9. Note the attached Information Disclosure Statemen	nt(s)(PTO-1449) Paper No(s)					
10. Other:						
	J	1/1				
		Keé M. Tung imary Examiner	•			





Continuation of 2. NOTE: The terms "each of the plurality of output portions of the output-side line memory has a plurality of memory units and is configure as a shift register..." requires both further consideration and search.

Continuation of 5. does NOT place the application in condition for allowance because: of the reasons given in the previous Office Action. Specifically, Koyama et al. teach a driving circuit for a semiconductor display device, comprising a data separation circuit, an input-side line memory sequentially receiving and storing data input, and an output-side line memory for for holding data transferred in parallel from input-side line memory, and capable of serially outputting data as analog display data.